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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10028972	FILING DATE 12/28/2001	CLASS 438	SUBCLASS	GAU 2310	EXAMINER
<p>**APPLICANTS: Lee, Ga</p> <p>**CONTINUING DATA VERIFIED:</p> <p>** FOREIGN APPLICATIONS VERIFIED: REPUBLIC OF KOREA 2001 35731 06 26 2001</p>					
PG PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> Yes <input type="checkbox"/> No 35 USC 119 conditions met <input type="checkbox"/> Yes <input type="checkbox"/> No Verified and Approved by Examiner's Office				ATTORNEY DOCKETING 054216 5019	
TITLE : Method of forming inter-dielectric layer in semiconductor device					

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G.
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		PREPARED FOR ISSUE	Application Examiner	
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